Channel-Hopping Scheme and Channel-Diverse Routing in Static Multi-Radio Multi-Hop Wireless Networks

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Abstract—In modern wireless networks with multiple orthogonal (non-overlapping) channels available, one essential performance topic is how to effectively exploit channel diversity to enable parallel communications. Generally, having a radio interface hop through all available channels produces better spectrum diversity than binding it permanently to one channel, at the cost of channel switching delays and potentially compromised network connectivity. Moreover, multi-hop communications become challenging due to the lack of a common rendezvous for discovering routes and the difficulty of relaying packets from hop to hop. In this paper, we propose a multi-radio channelhopping scheme (CHS) that preserves network connectivity. We prove that less than three radios are required by CHS in order to achieve good channel overlapping in widespread IEEE 802.11-based wireless systems. Corresponding channel-diverse routing (CDR) protocol is devised to realize efficient multi-hop communications. Simulation results demonstrate that the proposed CDR outperforms other strategies in static IEEE 802.11a multi-hop networking environments.

Index Terms—Channel diversity, multi-channel routing, multi-radio multi-hop wireless network, IEEE 802.11, prime field

1 BACKGROUND

TAT ITH the advent of ever-growing bandwidth-demanding services and applications, throughput performance remains a critical issue in the wireless networking community. One promising way for throughput improvement is to utilize multiple orthogonal (non-overlapping) channels available in a wireless system. Take the dominant wireless standard IEEE 802.11 for instance, three and eight (up to twelve) orthogonal channels can be utilized in IEEE 802.11b/g and 802.11a respectively. However, design of multi-channel protocols (in both single-radio and multi-radio networks) is nontrivial and deals with not only the link-layer channel assignment scheme but also the routing strategy for multihop communications [14]. Below we review several past multi-channel research works in the literature.

A static channel allocation scheme is proposed to bind each radio interface to a channel permanently (or for a long term) in [6]. The static binding is straightforward and indeed utilizes multiple channels simultaneously, but this approach requires as many radio interfaces as available channels, which is barely possible in real practices. As a result, several optimization models, targeting on wireless mesh networks (WMNs), are designed to enhance network performance under the constraint of limited radio interfaces [2], [17], [22]. These works

also permanently (statically) bind each radio to a dedicated channel. Since the majority of data packets in a WMN are destined for or originated from the Internet via gateways, the multi-channel protocols can thus be devised according to this unique traffic pattern. However, in general multi-hop wireless networks, communications can be any source-destination pair and static binding lacks the flexibility of reaching all available channels especially when traffic patterns are dynamic and unpredictable.

Rather than statically binding each interface, some multichannel protocols are proposed to dedicate one radio interface to a control channel (CC) and have the remaining one or more radios switch between usable data channels (DCs) [9], [12], [26]. Whenever there is a communications need, the intended transmitter and receiver negotiate a DC to use via CC in an on-demand manner. Two attendant drawbacks come with this approach. First, depending on the number of DCs, the CC may become underutilized or too congested (bottleneck effect). Second, communications overhead is high, since the negotiation is exercised on a per-packet basis. To overcome the control channel (CC) problems, some propose to have one interface fixed/assigned to a (data) channel permanently (or relatively a long time) for receiving data, and the remaining interface(s) switch between other channels [15], [20]. The fixed interface is called receiving interface, and the remaining as switchable interface(s). Without a CC, an intended transmitter tunes its switchable interface to the channel used by the receiving interface of an intended receiver. In this way, the CC underutilization and bottleneck problems can be avoided, but the performance now greatly depends on the receiving channel allocation scheme. If a receiving interface is not configured wisely, the system performance can be negatively compromised.

Next, we introduce another genre of multi-channel protocols that requires time synchronization between nodes.

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In [23], the authors propose a multi-channel protocol using a single transceiver (radio interface). The idea is to embed a negotiation phase in the ATIM (Ad-hoc Traffic Indication Map) window that is periodically sent under the Power Save Mode (PSM). Every node has to hop on a predefined control channel (CC) when entering the ATIM window. The negotiation phase is to determine a data channel (DC) to use after the ATIM window finishes. This approach, termed as split phase based on the classifications in [19], incurs both the CC problems and high overhead as most CC-based protocols do. Without using a CC, channel-hopping schemes are another alternative for single-radio multi-channel protocols [24], [25]. Dividing the time axis into virtual slots, all nodes perform channel switching by following a *common hopping sequence*. Two nodes stop hopping and start transmitting on the current channel whenever there is a communications need. This common hopping approach obviously guarantees network connectivity, but becomes inefficient when multiple nodes attempt to transmit simultaneously in the neighborhood. As a result, authors in [3] propose to use *distinct hopping sequences*, governed by randomly chosen (channel, seed) pairs. By using a prime number of channels and having nodes cycle through all channels based on predefined hopping offsets (seeds), this approach nicely preserves the network connectivity while enabling simultaneous transmissions in the neighborhood. However, due to lack of a CC, broadcast transmissions become difficult and consequently multi-hop routing packets cannot efficiently propagate. Moreover, channel switching overhead should also be considered in channel-hopping schemes.

In this paper, we adopt the channel-hopping scheme with distinct hopping sequences to better exploit channel diversity. Several major contributions are made in this work. First, we propose a channel-hopping scheme (CHS) for multi-radio networking environments. CHS leverages the connectivity-preserving methodology introduced in [3] and extends the method to multi-radio hopping sequences design. Our goal is to increase the overlapping ratio between nodes by utilizing multiple radios while maintaining reasonable channel randomness. Intuitively more radios lead to more overlapping time slots. Given *p* orthogonal channels used by CHS, we prove that \sqrt{p} radios (tantamount to less than three radios in IEEE 802.11-based systems) are required to achieve the best case of overlapping. Second, corresponding channel-diverse routing (CDR) is devised to realize efficient multi-hop data delivery under this channel-hopping system. We propose to embed a broadcast slot running over a control channel (CC) to facilitate broadcast transmissions. The CC is dedicated to exchanging control information, which prevents CDR from having the congestion (bottleneck) problem due to frequent data channel negotiations. In fact, the usage of CC is beneficial in providing a common rendezvous for control packets, such as ad-hoc beacons, hopping table exchange messages, routing requests, etc. For route selections, CDR takes both channel quality and overlapping efficiency from hop to hop into consideration, and decides on a route with the minimum average expected transmission time (ETT) from source to destination. Third, we propose to implement per-destination queues and perform selective round-robin (SRR) mechanism for queue services. In channelhopping schemes, data queue management is critical in avoiding blocking other transmittable packets due to a HOL

(Head-of-Line) packet whose receiver is currently out of reach. By exercising proper data queue management, we aim to transmit as many packets as possible in a single time slot.

The remainder of this paper is organized as follows. In Section 2, we state the assumptions made by this work and the key problems that pertain to our designs. Sections 3 and 4 detail the proposed channel-hopping scheme (CHS) and channel-diverse routing (CDR) protocol, respectively. Simulation results are presented in Section 5 for performance evaluation and comparison with other multi-channel approaches. Finally, Section 6 draws our concluding remarks.

2 ASSUMPTIONS AND PROBLEM STATEMENT

Before elaborating on the proposed multi-channel protocols, we make the following assumptions for the target wireless environment.

- All nodes use half-duplex radio interfaces (transceivers) that are switchable between channels. When switching to a different channel, a transceiver takes about 80 to 200 200 s, depending on the hardware technology, for its circuits to stabilize [1], [10]. Adding to this delay is the link-layer waiting time, which is necessary for mitigating the multi-channel hidden-terminal problem, collectively we define the time spent on one channel switching the *switching delay*, denoted as t_{SDE} .
- We divide the time axis into time slots with equal *slot duration*, denoted as t_{SDU} . All nodes implement time synchronization mechanism based on reference broad-casts in order to run the channel-hopping scheme [8].
- Given *p* data channels, denoted as C₀, C₁,..., C_{p-1}, the achievable rate R_i associated with channel C_i, where 0 ≤ i < p, can be estimated through packet probing mechanism introduced in [5]¹.

Fig. 1 illustrates a sample single-radio network with five orthogonal channels (Ch 0, Ch 1, ..., Ch 4) having different link rates (ranging from 2 to 18 Mbps). The time axis is divided into five-slot cycles in order to go through the five channels. Each node randomly chooses its own channel-hopping schedule as displayed in Fig. 1 (lower left). Unfortunately, because of disjoint channel-hopping schedules chosen, nodes A and G become disconnected despite the fact that they are within the communications range, which is not desirable in a generalpurpose wireless network. Thus the first problem we face is how to design a channel-hopping scheme that allows nodes to independently decide their own channel schedule and preserves network connectivity at the same time? Now, suppose node A (source) needs to communicate with node D (destination) and three possible routes are discovered: $A \rightarrow B \rightarrow D$, $A \rightarrow E \rightarrow D$, and $A \rightarrow C \rightarrow F \rightarrow D$. As shown in Fig. 1 (right), for the first hop of route $A \rightarrow B \rightarrow D$, nodes A and B can only communicate at Slot 3 over middle-quality Ch 2. On the other hand, nodes A and E in route $A \to E \to D$ have two overlapping slots (Slot 4 and 5) over low-quality Ch 3 and Ch 4, while nodes A and C in route A \rightarrow C \rightarrow F \rightarrow D also have two

^{1.} Note that R_i indicates the channel capacity (maximal achievable link rate) for C_i . Our packet probing mechanism constantly measures and updates data rates for adjacent links over channel C_i according to current communication distance, background noise, and channel conditions between the intended transmitter and receiver.

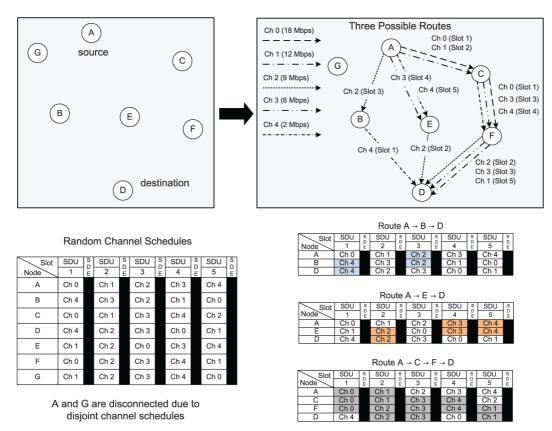


Fig. 1. Illustration of the multi-channel issues in a multi-hop wireless network, where each node has single radio with randomly-chosen channel-hopping schedule repeated in five-slot cycles and five orthogonal channels are available in the system.

overlapping slots (Slot 1 and 2) over high-quality Ch 0 and Ch 1. More overlapping slots imply more delivery opportunities. Meanwhile, with the same number of overlapping slots, channels having better qualities (higher link rates) are preferred because more packets may be delivered in a single time slot. Consequently, in our sample network, route $A \rightarrow C \rightarrow F \rightarrow D$ can potentially act as the best route among the three, despite that it has one more hop than the other two routes. The notion that hop count no longer serves as the single metric in rate-diverse multi-hop networks has been investigated in [7]. Therefore the second problem we need to solve is *how to devise a multi-hop routing protocol that factors in both the overlapping conditions and channel qualities (link rates) to determine a best route under such channel-hopping environment?*

Based on the foregoing observations, we aim to develop a connectivity-preserving channel-hopping scheme (CHS) for the first problem, and an efficient channel-diverse routing (CDR) for the second problem. Note that in practical wireless networks, channel allocation and routing mechanisms tend to interact with each other. As a result, we propose to design a complete multi-channel protocol suite that takes care of both mechanisms in a unified framework.

3 CHANNEL-HOPPING SCHEME (CHS)

In this section, we propose a channel-hopping scheme (CHS) that guarantees network connectivity. The channel overlapping behavior in CHS follows the properties of *prime field*, subfield of the Galois field, which is reviewed in Section 3.1. Based on the arithmetic operations of some prime field,

Section 3.2 and 3.3 present how to configure channel-hopping schedule for each interface in single-radio and multi-radio networks respectively². Mathematical analysis is provided in Section 3.4 to investigate the overlapping properties of our channel-hopping design and provide insight into the relationship of available number of channels and required radio interfaces.

3.1 Properties of the Galois Field

Fields are abstractions of number systems (set \mathbb{F}) and their essential operations (addition and multiplication) [11]. Familiar examples of fields include the rational numbers, the real numbers, and the complex numbers, all with infinite elements in \mathbb{F} . If the set \mathbb{F} is finite, then the field is said to be *finite*. For the finite fields, Galois proved their existence and concluded that the number of elements in a finite field must be a prime power, say $q = p^r$, where p is a prime and r is a positive integer. Define the Galois field $\mathbb{GF}(q)$. If r = 1, then the Galois field becomes $\mathbb{GF}(p)$, known as the prime field $\mathbb{GF}(p)$, which is isomorphic to the integers modulo p (where p is called the modulus).

In a prime field $\mathbb{GF}(p)$, the addition operation \oplus and multiplication operation \odot are defined as follows.

Definition 1. $\forall a, b \in \mathbb{F}$, $a \oplus b \triangleq (a + b) \mod p$ and $a \odot b \triangleq (a \cdot b) \mod p$, where for any integer x, $x \mod p$

^{2.} In the literature, the usage of quorum-based concept is another possible direction for designing overlapped channel-hopping schedules in single-radio wireless systems [4], [16], [18]. In this paper, we adopt the prime-based hopping-sequence design for our CHS and extend it to support multi-radio wireless systems.

Additive Inverse (Negative) -0 = 0-1 = 4-2 = 3-3 = 2- 4 = 1

\oplus	0	1	2	3	4
0	0	1	2	3	4
1	1	2	3	4	0
2	2	3	4	0	1
3	3	4	0	1	2
4	4	0	1	2	3
ļ	Addi	tion	in C	GF(5	5)

\odot	0	1	2	3	4	Multiplicative Inverse
0	0	0	0	0	0	1 ⁻¹ = 1
1	0	1	2	3	4	2 ⁻¹ = 3
2	0	2	4	1	3	3 ⁻¹ = 2
3	0	3	1	4	2	4 ⁻¹ = 4
4	0	4	3	2	1	
Mu	Itipli	cati	on iı	n GF	-(5)	

1 - 1
2 ⁻¹ = 3
3 ⁻¹ = 2
4 ⁻¹ = 4

Fig. 2. Operations of addition and multiplication in $\mathbb{GF}(5)$.

denotes the unique integer remainder r obtained upon dividing xby $p \ (0 \le r \le p-1)$, known as the reduction modulo p.

Like the usual integer operations, addition and multiplication in a prime field follow the associative, commutative, distributive laws, and the operation results are also elements in F (closeness property). Additive identity is 0, whereas multiplicative identity is 1. Subtraction of field elements is defined in terms of addition, while division of filed elements is defined in terms of multiplication. Below we have the definition for subtraction and division operations in a prime field.

Definition 2. $\forall a, b \in \mathbb{F}, a - b = a \oplus (-b)$ where -b is the additive inverse (negative) of b, and for $b \neq 0$, $a/b = a \odot b^{-1}$ where b^{-1} is the multiplicative inverse of b.Recall that the additive inverse -b is an element in \mathbb{F} such that $b \oplus (-b) = 0$ (0 is the additive identity), while the multiplicative inverse b^{-1} is an element in in \mathbb{F} such that $b \odot b^{-1} = 1$ (1 is the multiplicative identity).

Fig. 2 shows an example of prime field GF(5) with the addition \oplus and multiplication \odot operations, where the elements of \mathbb{F} are $\{0, 1, 2, 3, 4\}$. Those shaded areas display the results that lead to the additive or multiplicative identity, such that both the additive and multiplicative inverse for each element can be easily obtained from a lookup in this table. Note that no multiplicative inverse for element 0 exists as defined in Definition 2. We will revisit these arithmetic operations of a prime field in Section 3.4.

3.2 Single-Radio Channel-Hopping Design

Suppose *p* channels are available, denoted as $C_0, C_1, \ldots, C_{p-1}$, and define $\chi^{A}[k]$ as the channel ID used at the k^{th} time slot in a cycle for node A, where $1 \le k \le p$. In this section, we consider the channel-hopping design for the single radio interface equipped at each node. Based on the elements $\{0, 1, \ldots, \}$ p-1 contained in \mathbb{F} of a prime field, theoretically p! random sequences can be generated. However, two randomly generated sequences cannot guarantee overlapping among elements (as shown in Fig. 1). Thus we only generate pseudorandom sequences, for each of which a starting channel (SC) and hopping offset (Seed) are predefined, as the legitimate channel-hopping schedules. Specifically, if node A selects its starting channel (SC) as χ^A and hopping offset (Seed) as α^A , then all channels used in a cycle in order can be inferred by computing $\chi^A[k] = \chi^A \oplus \alpha^A(k-1)$, where $1 \le k \le p$. Three nice properties are possessed by pseudo-random sequences. First, since *p* is a prime, all channels are guaranteed to be visited *exactly once* in a cycle and repeated in the following cycles (i.e., $\chi^{A}[k+np] = \chi^{A}[k]$ for positive integer *n*), known as the periodicity property [11]. Second, the pseudo-random

sequence allows a representation using a fixed-length (SC, Seed) pair, independent of number of available channels p. In other words, no matter how long a cycle can be, a (SC, Seed) pair is sufficient for expressing the channel-hopping schedule in use, which is beneficial in bounding the communications overhead spent on schedule exchange between nodes. Third, for any two nodes with different seed values, one overlapping *channel is guaranteed to occur in a cycle* (in-depth mathematical proof will be provided in Section 3.4). For instance, given p = 5, sequences $\{0, 1, 2, 3, 4\}$ with (SC, Seed) = (0, 1) and $\{3, 0, 2, 4, 1\}$ with (SC, Seed) = (3, 2) are both legitimate channel-hopping schedules, which satisfy the periodicity property and overlap at the third slot on channel C_2 .

Under this channel-hopping design, however, two nodes, say A and B, can still become disconnected if they happen to choose different starting channels ($\chi^A \neq \chi^B$) but the same seed ($\alpha^A = \alpha^B$). In this case, the two nodes always hop on distinct channels (separated by a distance of $|\chi^A - \chi^B|$) and never come into contact. To address this problem, a Parity Slot is introduced as Slot 0 operating on the parity channel β , which is set equal to the seed α . As a result, one more slot is added into a cycle, leading to (p + 1) slots, and the channel $\chi^{A}[k]$ visited at the k^{th} slot for node A can be derived as follows,

$$\chi^{A}[k] = \begin{cases} \alpha^{A} & \text{for } h = 0\\ \chi^{A} \oplus \alpha^{A} \odot (h-1) & \text{for } 1 \le h \le p \end{cases}, \quad (1)$$

where $h = k \mod (p+1)$.

Fig. 3 gives an example. Nodes A, B, and C choose their channel-hopping schedules by defining their own (SC, Seed) pairs. Unfortunately nodes B and C turn out to have disjoint hopping schedules because they choose different SCs with the same Seed. By inserting a Parity Slot, operating on the parity channel β , at the beginning of each cycle, nodes B and C are then made connected through Ch 2.

3.3 Multi-Radio Channel-Hopping Design

Now we extend the channel-hopping design to support multiradio environments. Suppose w radio interfaces are available at each node. For some node A, denote these interfaces as $I_1^A, I_2^A, \ldots, I_w^A$. Our goal is to determine appropriate (SC, Seed) pair and the parity channel for each interface. Adopting similar notation system from Section 3.2, define χ_i^A , α_i^A , and β_i^A to represent the starting channel (SC), hopping offset (Seed), and parity channel for interface I_i^A at node A, where $1 \le i \le w$. First of all, we use the single-radio channelhopping design for configuring the first interface I_1^A by setting χ_1^A , α_1^A to generate a pseudo-random sequence and letting

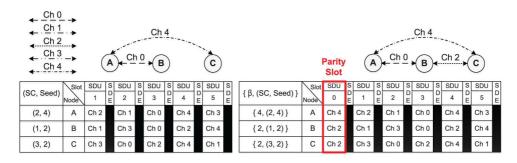


Fig. 3. Single-radio channel-hopping schedules for nodes A, B and C, where nodes B and C are disconnected due to disjoint hopping schedules (left). By introducing a Parity Slot (Slot 0), connectivity between nodes B and C can be preserved (right).

6:

7:

 $\beta_1^A = \alpha_1^A$ for ensuring connectivity between disjoint schedules. Recall that two schedules are disjoint if they have different SCs with the same Seed. We then leverage this property for configuring the remaining of interfaces at node A, because we certainly do not want interfaces equipped at the same node to hop on the same channel at the same time slot. Specifically, for $2 \le i \le w$, α_i^A is made equal to α_1^A , and χ_i^A is suggested to be set as $\chi_i^A = \chi_1^A \oplus \alpha_i^A \odot (\lceil p/w \rceil \odot (i-1))$ with the objective of distributing the starting channels of all interfaces. Next we consider the parity channels β_i^A for $2 \le i \le w$. Since using the same channel at all interfaces in the parity slot is not desirable, we propose to set $\beta_i^A = \alpha_1^A \odot i$ to guarantee distinct channels are associated with all interfaces in the parity slot. This property is easy to prove (based on the fact that set $\{\beta_1^A, \beta_2^A, \dots, \beta_w^A\}$ is a subset of $\{0, 1, 2, \dots, p-1\}$ and each element in set $\{\beta_1^A, \beta_2^A, \dots, \beta_w^A\}$ appears exactly once), considering $w \leq p$ in practical multi-radio networks. Consequently, all radio interfaces have been configured with appropriate channel-hopping schedules, and the channel $\chi_i^A[k]$ visited by interface I_i^A at the k^{th} slot for node A can be obtained by the following,

$$\chi_i^A[k] = \begin{cases} \alpha_1^A \odot i & \text{for } h = 0\\ \chi_1^A \oplus \alpha_1^A \odot \left(\lceil p/w \rceil \odot (i-1) \oplus (h-1) \right) & \text{for } 1 \le h \le p \end{cases}$$
(2)

where $h = k \mod (p+1)$.

Fig. 4 illustrates the channel schedules and overlapping conditions for two flows (A \rightarrow B and C \rightarrow D). Fig. 4(a) shows that, given p = 5 and p = 11, only one overlapping slot in a cycle is available for each flow when a single radio interface (w = 1) is equipped at each node. By adding one more radio interface, making w = 2, and exercising our CHS for configuring the two radios at each node, the overlapping slots (and usable channels) have been significantly increased. As displayed in Fig. 4(b), the high overlapping ratio is beneficial especially when the cycle is long (due to a large p). From the figure, we observe that CHS effectively enhances transmission opportunities between nodes at the cost of limited hardware (one more radio), while maintaining high level of channel diversity.

Algorithm 1 Channel Schedule Configuration at Node A

- 1: Given *p* channels and ω radio interfaces ($\omega \leq p$);
- 2: Randomly select the starting channel χ_1^A from [0, p-1];
- 3: Randomly select the seed α_1^A from [0, p-1];

4: Set parity channel
$$\beta_1^A = \alpha_1^A$$
;

5: **if** $(\omega > 1)$ **then**

for
$$(i = 2; i \leq \omega; i + +)$$
 do

Set $\alpha_i^A = \alpha_1^A$;

8: Set
$$\chi_i^A = \chi_1^A \oplus \alpha_i^A \odot \left(\left\lceil \frac{p}{\omega} \right\rceil \odot (i-1) \right)$$

9: Set $\beta_i^A = \alpha_1^A \odot i$;

10: **end for**

11: end if

Thus far we have completed the descriptions of our multiradio channel-hopping design. Algorithm 1 provides the pseudocode for the proposed channel-hopping scheme (CHS) operations carried out by a node with w radio interfaces and p orthogonal channels available in the system.

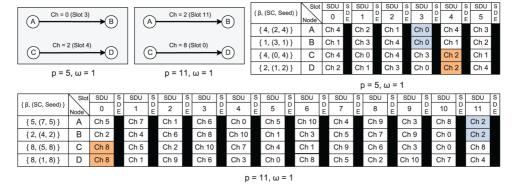
3.4 Mathematical Analysis

In this section, we analyze the mathematical properties of the proposed channel-hopping scheme (CHS) and prove that network connectivity is well preserved under this scheme. Given *p* orthogonal channels with *p* prime, there are (p + 1) slots (including the parity slot at Slot 0) contained in a cycle. Below we start with the single-radio system and then extend the analysis to multi-radio channel-hopping design.

3.4.1 Connectivity Properties of Single-radio CHS

For any two neighboring nodes A and B each equipped with a single radio, three cases are possible for the relationship between their channel schedules. The three cases are $\alpha^A = \alpha^B$ and $\chi^A = \chi^B$, $\alpha^A = \alpha^B$ but $\chi^A \neq \chi^B$, and $\alpha^A \neq \alpha^B$ (whether $\chi^A = \chi^B$ or $\chi^A \neq \chi^B$). We prove that the proposed single-radio CHS is able to preserve network connectivity in all three cases.

- **Lemma 1.** When $\alpha^A = \alpha^B$ and $\chi^A = \chi^B$, (p+1) slots overlap in a cycle between nodes A and B.
- **Proof.** In this case, nodes A and B have *identical* channelhopping schedule. In addition, parity channel $\beta^A = \beta^B$ because $\beta^A = \alpha^A$, $\beta^B = \alpha^B$, and $\alpha^A = \alpha^B$. According to Eq. (1), when $\alpha^A = \alpha^B$ and $\chi^A = \chi^B$, apparently any channel $\chi^A[k]$ visited at the k^{th} slot by node A overlaps with channel $\chi^B[k]$ visited by node B, leading to all (p+1) slots in a cycle are overlapped between nodes A and B.



				1		Ch =	4 (Slot 1)				3, (SC, See	ad) 1	Slot	SE		s	SDU	S D	SDL		SDU		S SDU	S	<u> </u>	
	(Slot 3)							Slot 4) Slot 5)				5, (50, 566	su)	Node	0)	D E	1	E	2	D E	3	E	4	E	5	D E
Ch = 0	, 4 (Slot (Slot 5)	· _	~		~			Slot 5) Slot 11)				{ 4, (2, 4)	}	Α	Ch	14		Ch 2		Ch	1	Ch (D	Ch 4		Ch 3	
(A)	. ,	→ (E	3)	110	୬		-		→(B		{ 3, (4, 4))}		Ch	n 3		Ch 4		Ch 3	3	Ch 2	2	Ch 1		Ch 0	
				c	:h =	8, 5 (Slot	t 0)	Ch = 1 (S	Slot	6)		{ 1, (3, 1))}		Ch	n 1		Ch 3		Ch 4	1	Ch (D	Ch 1		Ch 2	Т
Ch = 4	(Slot 0)					5 (Slot 1) 2 (Slot 2		Ch = 9 (5 Ch = 6 (5				{ 2, (1, 1)	}	В	Ch	12		Ch 1		Ch 2	2	Ch 3	3	Ch 4		Ch 0	
	(Slot 1) (Slot 2))h =	10 (Slot	3)	Ch = 3 (S	Slot	9)		{ 4, (0, 4))}		Ch	14		Ch 0		Ch 4	1	Ch 3	3	Ch 2		Ch 1	1
Ch = 0	(Slot 3)					7 (Slot 4 4 (Slot 5		Ch = 0 (S Ch = 8 (S			\vdash	{ 3, (2, 4)	· ·	С	Ch	13		Ch 2		Ch '	1	Ch ()	Ch 4		Ch 3	
(C) Ch = 2	(Slot 4)	→ (1	5)	110	5		ŕ		-	Ó	\vdash	{ 2, (1, 2)			Ch	12		Ch 1		Ch :	3	Ch (2	Ch 2		Ch 4	_
					_					\bigcirc	\vdash	{ 4, (2, 2)	-	- D	Ch			Ch 2		Ch 4		Ch 1		Ch 3		Ch 0	-
p = 5	i, ω =	2				p = 1	11,	ω = 2			L	(+, (2, 2)			01			0112		UII -		on		one	1	0110	
																		p =	5,	ω =	2						
	Slot	SDU	S	SDU	S	SDU	s	SDU	S	SDU	s	SDU	S	SDU	s	SDI	1	S :	SDU	S			<u> </u>	0011	01	SDU	S
	10101	300		500		000										000	_		500		51		3 L	SDU	S	500	
{ β, (SC, Seed) }	Node	0	DE	1	DE	2	DE		D E	4	DE		DE	0	D E	7	_	DE	8	DE	⊢			10	DE	11	DE
{ β, (SC, Seed) } { 5, (7, 5) }	Node		D		D		DE		D E	4 Ch 0	DE		D E	0	D E			D E		DE	9				D		DE
{ 5, (7, 5) } { 10, (4, 5) }		0 Ch 5 Ch 10	D	1 Ch 7 Ch 4	D	2 Ch 1 Ch 9	DE	3 Ch 6 Ch 3	DE	Ch 0 Ch 8	E	5 Ch 5 Ch 2	D	6 Ch 10 Ch 7	DE	7 Ch Ch	4 1	D E (8 Ch 9 Ch 6	D E	Cł Cł	9 [13 10		10 Ch 8 Ch 5	D E	11 Ch 2 Ch 10	
{ 5, (7, 5) } { 10, (4, 5) } { 2, (4, 2) }	Node	0 Ch 5 Ch 10 Ch 2	D	1 Ch 7 Ch 4 Ch 4	D	2 Ch 1 Ch 9 Ch 6	DE	3 Ch 6 Ch 3 Ch 8	DE	Ch 0 Ch 8 Ch 10	E	5 Ch 5 Ch 2 Ch 1	E	6 Ch 10 Ch 7 Ch 3	E	7 Ch Ch Ch	4 1 5	D E ()	8 Ch 9 Ch 6 Ch 7	D E	ch Ch Ch	9 [13 10 19		10 Ch 8 Ch 5 Ch 0	D E	11 Ch 2 Ch 10 Ch 2	
	A	0 Ch 5 Ch 10 Ch 2 Ch 4	D	1 Ch 7 Ch 4 Ch 4 Ch 5	D	2 Ch 1 Ch 9 Ch 6 Ch 7	DE	3 Ch 6 Ch 3 Ch 8 Ch 9	DE	Ch 0 Ch 8 Ch 10 Ch 0	E	5 Ch 5 Ch 2 Ch 1 Ch 2	E	6 Ch 10 Ch 7 Ch 3 Ch 4	E	7 Ch Ch Ch Ch	4 1 5 6		8 Ch 9 Ch 6 Ch 7 Ch 8	DE	Ch Ch Ch Ch	9 [1 3 1 0 1 9 10		10 Ch 8 Ch 5 Ch 0 Ch 1	D E	11 Ch 2 Ch 10 Ch 2 Ch 3	
	A	0 Ch 5 Ch 10 Ch 2 Ch 4 Ch 8	D	1 Ch 7 Ch 4 Ch 4 Ch 5 Ch 5	D	2 Ch 1 Ch 9 Ch 6 Ch 7 Ch 2	DE	3 Ch 6 Ch 3 Ch 8 Ch 9 Ch 10	DE	Ch 0 Ch 8 Ch 10 Ch 0 Ch 7	E	5 Ch 5 Ch 2 Ch 1 Ch 2 Ch 2 Ch 4	D	6 Ch 10 Ch 7 Ch 3 Ch 4 Ch 1	D E	7 Ch Ch Ch Ch	4 1 5 6 9		8 Ch 9 Ch 6 Ch 7 Ch 8 Ch 6		Ch Ch Ch Ch	9 E 1 3 1 0 1 9 10 10 13		10 Ch 8 Ch 5 Ch 0 Ch 1 Ch 0	D E	11 Ch 2 Ch 10 Ch 2 Ch 3 Ch 8	
	A B	0 Ch 5 Ch 10 Ch 2 Ch 4 Ch 8 Ch 5	D	1 Ch 7 Ch 4 Ch 4 Ch 5 Ch 5 Ch 9	D	2 Ch 1 Ch 9 Ch 6 Ch 7 Ch 2 Ch 6	DE	3 Ch 6 Ch 3 Ch 8 Ch 9 Ch 10 Ch 10	DE	Ch 0 Ch 8 Ch 10 Ch 0 Ch 7 Ch 0	E	5 Ch 5 Ch 2 Ch 1 Ch 2 Ch 2 Ch 4 Ch 8	D E	6 Ch 10 Ch 7 Ch 3 Ch 4 Ch 4 Ch 1 Ch 5		7 Ch Ch Ch Ch Ch	4 1 5 6 9 2		8 Ch 9 Ch 6 Ch 7 Ch 8 Ch 8		Ch Ch Ch Ch Ch	9 E 1 3 1 0 1 9 1 10 1 3 1 7		10 Ch 8 Ch 5 Ch 0 Ch 1 Ch 1 Ch 0 Ch 4	D E	11 Ch 2 Ch 10 Ch 2 Ch 3 Ch 3 Ch 8 Ch 1	
$ \{5, (7, 5)\} $ $ \{10, (4, 5)\} $ $ \{2, (4, 2)\} $ $ \{4, (5, 2)\} $ $ \{8, (5, 8)\} $ $ \{5, (9, 8)\} $ $ \{8, (1, 8)\} $	A B C	0 Ch 5 Ch 10 Ch 2 Ch 4 Ch 8 Ch 5 Ch 8	D	1 Ch 7 Ch 4 Ch 5 Ch 5 Ch 9 Ch 1	D	2 Ch 1 Ch 9 Ch 6 Ch 7 Ch 2 Ch 6 Ch 9	DE	3 Ch 6 Ch 3 Ch 8 Ch 9 Ch 10 Ch 3 Ch 6	DE	Ch 0 Ch 8 Ch 10 Ch 0 Ch 7 Ch 0 Ch 3	E	5 Ch 5 Ch 2 Ch 1 Ch 2 Ch 4 Ch 8 Ch 0	D E	6 Ch 10 Ch 7 Ch 3 Ch 4 Ch 4 Ch 1 Ch 5 Ch 8		7 Ch Ch Ch Ch Ch Ch	4 1 5 9 2 5		8 Ch 9 Ch 6 Ch 7 Ch 8 Ch 6 Ch 6 Ch 1 Ch 2		Ch Ch Ch Ch Ch Ch	9 E 1 3 1 0 1 9 1 0 1 3 1 7 1 0		10 Ch 8 Ch 5 Ch 0 Ch 1 Ch 0 Ch 4 Ch 7	D E	11 Ch 2 Ch 10 Ch 2 Ch 3 Ch 3 Ch 8 Ch 1 Ch 4	
	A B	0 Ch 5 Ch 10 Ch 2 Ch 4 Ch 8 Ch 5	D	1 Ch 7 Ch 4 Ch 4 Ch 5 Ch 5 Ch 9	D	2 Ch 1 Ch 9 Ch 6 Ch 7 Ch 2 Ch 6		3 Ch 6 Ch 3 Ch 8 Ch 9 Ch 10 Ch 10	DE	Ch 0 Ch 8 Ch 10 Ch 0 Ch 7 Ch 0	E	5 Ch 5 Ch 2 Ch 1 Ch 2 Ch 2 Ch 4 Ch 8	D E	6 Ch 10 Ch 7 Ch 3 Ch 4 Ch 4 Ch 1 Ch 5		7 Ch Ch Ch Ch Ch	4 1 5 9 2 5		8 Ch 9 Ch 6 Ch 7 Ch 8 Ch 8		Ch Ch Ch Ch Ch Ch	9 E 1 3 1 0 1 9 1 10 1 3 1 7		10 Ch 8 Ch 5 Ch 0 Ch 1 Ch 1 Ch 0 Ch 4	D E	11 Ch 2 Ch 10 Ch 2 Ch 3 Ch 3 Ch 8 Ch 1	

Fig. 4. Demonstration of high overlapping ratio achieved by our channel-hopping design, where (a) shows the single-radio channel schedules for p = 5 and p = 11 respectively and (b) displays the channel schedules after adding one more radio and performing the proposed multi-radio channel-hopping scheme (CHS).

(b)

- **Lemma 2.** When $\alpha^A = \alpha^B$ and $\chi^A \neq \chi^B$, there exists one overlapping slot (at the parity slot) in a cycle between nodes *A* and *B*.
- **Proof.** In this case, nodes A and B have *disjoint* channelhopping schedules and hop in parallel without coming into contact. According to Eq. (1), the parity channel visited at the parity slot is overlapped due to $\alpha^A = \alpha^B$. For non-parity slots in a cycle, suppose there exists an overlapping slot with offset K_{AB} with respect to Slot 1, where $0 \le K_{AB} \le p$. According to Eq. (1), we have $\chi^A \oplus \alpha^A \odot K_{AB} = \chi^B \oplus \alpha^B \odot K_{AB}$. Based on the arithmetic operations of a prime field reviewed in Section 3.1, the overlapping slot offset K_{AB} can be derived as

$$K_{AB} = (\chi^A \oplus (-\chi^B)) \odot (\alpha^B \oplus (-\alpha^A))^{-1}.$$
 (3)

Since $\alpha^A = \alpha^B$, K_{AB} does not exist. In other words, no overlapping slot exists among non-parity slots. As a result, when $\alpha^A = \alpha^B$ and $\chi^A \neq \chi^B$, one overlapping slot (at the parity slot) exists in a cycle.

Lemma 3. When $\alpha^A \neq \alpha^B$, there exists one overlapping slot (at non-parity slot) in a cycle between nodes A and B.

- **Proof.** According to Eq. (1), the parity channels visited at the parity slot by nodes A and B are different because $\alpha^A \neq \alpha^B$. For non-parity slots in a cycle, suppose there exists an overlapping slot with offset K_{AB} with respect to Slot 1, where $0 \leq K_{AB} < p$. Based on similar calculations from Lemma 2, we have $K_{AB} = (\chi^A \oplus (-\chi^B)) \odot (\alpha^B \oplus (-\alpha^A))^{-1}$ which is obtainable since $\alpha^A \neq \alpha^B$ (no matter $\chi^A = \chi^B$ or $\chi^A \neq \chi^B$). In other words, nodes A and B are expected to meet at Slot $(K_{AB} + 1)$ over channel $\chi^A[K_{AB} + 1]$ (= $\chi^B[K_{AB} + 1]$) among non-parity slots per cycle. As a result, when $\alpha^A \neq \alpha^B$, one overlapping slot (at non-parity slot) exists in a cycle between nodes A and B. \Box
- **Theorem 1.** Network connectivity is guaranteed in the singleradio CHS.
- **Proof.** By Lemma 1, Lemma 2, and Lemma 3, there exists at least one overlapping slot in a cycle between any two neighboring nodes, leading to a guaranteed network connectivity produced by the proposed single-radio channel-hopping scheme (CHS).

3.4.2 Connectivity Properties of Multi-radio CHS

For any two neighboring nodes A and B with w radios available $(1 \le w \le p)$, four cases are analyzed separately.

(a)

These cases include $\alpha_1^A = \alpha_1^B$ and $\chi_1^A = \chi_1^B$, $\alpha_1^A = \alpha_1^B$ but $\chi_1^A \neq \chi_1^B$, $\alpha_1^A \neq \alpha_1^B$ with $\alpha_1^A \neq (-\alpha_1^B)$, and $\alpha_1^A \neq \alpha_1^B$ with $\alpha_1^A = (-\alpha_1^B)$. We prove that the proposed multi-radio CHS is capable of preserving network connectivity in all cases.

- **Lemma 4.** When $\alpha_1^A = \alpha_1^B$ and $\chi_1^A = \chi_1^B$, there are (p+1) overlapping channels in a cycle between nodes A and B.
- **Proof.** Based on the multi-radio CHS design, if $\alpha_1^A = \alpha_1^B$ and $\chi_1^A = \chi_1^B$, then $\alpha_i^A = \alpha_i^B$ and $\chi_i^A = \chi_i^B$ for interfaces I_i^A and I_i^B equipped at nodes A and B respectively, where $1 \le i \le w$. In other words, for any index *i*, I_i^A and I_i^B adopt an identical channel-hopping schedule. According to Eq. (2), apparently any channel $\chi_i^A[k]$ visited at the k^{th} slot by interface I_i^A overlaps with channel $\chi_i^B[k]$ visited by interface I_i^B , leading to all (p+1) slots in a cycle are overlapped between nodes A and B.
- **Lemma 5.** When $\alpha_1^A = \alpha_1^B$ and $\chi_1^A \neq \chi_1^B$, there exists at least one overlapping slot (at the parity slot) in a cycle between nodes A and B.
- **Proof.** Based on the multi-radio CHS design, if $\alpha_1^A = \alpha_1^B$ and $\chi_1^A \neq \chi_1^B$, then $\alpha_i^A = \alpha_i^B$ and $\chi_i^A \neq \chi_i^B$ for interfaces I_i^A and I_i^B equipped at nodes A and B, where $1 \le i \le w$. According to Eq. (2), the parity channel visited at the parity slot by interfaces I_i^A and I_i^B is the same due to $\alpha_1^A = \alpha_1^B$. For non-parity slots in a cycle, suppose there exists an overlapping slot with offset $K_{A^iB^j}$ with respect to Slot 1 produced by interfaces I_i^A and I_i^B , where $0 \le K_{A^iB^j} < p$. According to Eq. (2), we have

$$\chi_1^A \oplus \alpha_1^A \odot (\lceil p/w \rceil \odot (i-1) \oplus K_{A^i B^j}) = \chi_1^B \oplus \alpha_1^B \odot (\lceil p/w \rceil \odot (j-1) \oplus K_{A^i B^j}).$$
(4)

Since $\alpha_1^A = \alpha_1^B$ and $\chi_1^A \neq \chi_1^B$, Eq. (4) does not hold if i = j, and holds if $j - i = (\chi_1^A \oplus (-\chi_1^B)) \odot (\alpha_1^A \odot [p/w])^{-1}$. In other words, no overlapping slot exists among non-parity slots between interfaces I_i^A and I_i^B , but non-parity overlapping occurs between interfaces I_i^A and I_j^B if $j - i = (\chi_1^A \oplus (-\chi_1^B)) \odot (\alpha_1^A \odot [p/w])^{-1}$. As a result, when $\alpha_1^A = \alpha_1^B$ and $\chi_1^A \neq \chi_1^B$, at least one overlapping slot (at the parity slot) exists in a cycle between nodes A and B.

- **Lemma 6.** When $\alpha_1^A \neq \alpha_1^B$ with $\alpha_1^A \neq (-\alpha_1^B)$, there exists at least one and at most $min(p, w^2)$ overlapping slots among non-parity slots in a cycle between nodes A and B.
- **Proof.** For the first interfaces I_1^A and I_1^B , based on Lemma 3, when $\alpha_1^A \neq \alpha_1^B$, one overlapping among non-parity slots in a cycle is guaranteed. For other overlapping opportunities, since $\alpha_1^A \neq \alpha_1^B$, from Eq. (4), we can derive

$$K_{A^{i}B^{j}} = [\chi_{1}^{A} \oplus (-\chi_{1}^{B}) \\ \oplus \alpha_{1}^{A} \odot (\lceil p/w \rceil \odot (i-1)) \oplus (-\alpha_{1}^{B}) \odot (\lceil p/w \rceil \\ \odot (j-1))] \odot (\alpha_{1}^{B} \oplus (-\alpha_{1}^{A}))^{-1}.$$
(5)

In other words, for any (i, j) pair, the overlapping offset $K_{A^iB^j}$ produced by interfaces I_i^A and I_j^B , where $1 \le i, j \le w$, is obtainable with at most $min(p, w^2)$ distinct values. As a result, when $\alpha_1^A \ne \alpha_1^B$ with $\alpha_1^A \ne (-\alpha_1^B)$, at least one and at most $min(p, w^2)$

non-parity slots are overlapped in a cycle between nodes A and B.

- **Lemma 7.** When $\alpha_1^A \neq \alpha_1^B$ with $\alpha_1^A = (-\alpha_1^B)$, there exists at least one and at most $min(p, \frac{w^2+w}{2})$ overlapping slots among non-parity slots in a cycle between nodes A and B.
- **Proof.** For the first interfaces I_1^A and I_1^B , based on Lemma 3, when $\alpha_1^A \neq \alpha_1^B$, one overlapping among non-parity slots in a cycle is guaranteed. For other overlapping opportunities, since $\alpha_1^A \neq \alpha_1^B$, we also obtain Eq. (5) as in Lemma 6. However, since $\alpha_1^A = (-\alpha_1^B)$ in this case, we can further reduce Eq. (5) into the following formula,

$$K_{A^{i}B^{j}} = [\chi_{1}^{A} \oplus (-\chi_{1}^{B}) \oplus \alpha_{1}^{A} \odot (\lceil p/w \rceil \odot (i+j-2))] \odot (2\alpha_{1}^{B})^{-1}.$$
(6)

Based on Eq. (6), we observe that $K_{A^iB^j} = K_{A^jB^j}$ due to the fact i + j = j + i. In other words, the overlapping produced by interfaces I_i^A and I_j^B is exactly the same as that produced by interfaces I_j^A and I_i^B , leading to at most $min(p, \frac{w^2+w}{2})$ overlapping non-parity slots. As a result, when $\alpha_1^A \neq \alpha_1^B$ with $\alpha_1^A = (-\alpha_1^B)$, at least one and at most $min(p, \frac{w^2+w}{2})$ non-parity slots are overlapped in a cycle between nodes A and B.

Corollary 1. From Lemma 6 and Lemma 7, suppose the best case of $min(p, w^2)$ slots overlapping among non-parity slots occurs in a cycle, then the required number of radio interfaces is upper bounded by \sqrt{p} under the CHS design.

Proof. It follows that $w^2 \leq p$, resulting in $w \leq \sqrt{p}$.

- **Theorem 2.** Network connectivity is guaranteed in the multiradio CHS.
- **Proof.** By Lemma 4, Lemma 5, Lemma 6, and Lemma 7, there exists at least one overlapping slot in a cycle between any two neighboring nodes, leading to a guaranteed network connectivity produced by the proposed multi-radio channel-hopping scheme (CHS).

4 CHANNEL-DIVERSE ROUTING (CDR)

Once channel-hopping schedules are determined, our channel-diverse routing (CDR) comes into play. Several aspects are involved in the CDR protocol. In Section 4.1, we make clear how control packets can be exchanged to gather network information in such channel-hopping environment without a dedicated radio interface for this kind communications traffic. Section 4.2 elaborates on the route discovery procedure, while Section 4.3 presents the technique of managing data queues to further improve delivery efficiency. We summarize the CDR protocol in Section 4.4.

4.1 Network Information Construction

In typical channel-hopping schemes (with distinct hopping schedules), channel diversity can be better exploited but transmitting broadcast packets becomes difficult/complicated due to the lack of a common communications rendezvous [3]. Observing this, we propose to set aside a control channel (CC) and add a Broadcast Slot at Slot (p + 1), extending a cycle to include (p + 2) time slots, as shown in Fig. 5.

(SC, Seed) of (SC, Seed) of		Slot		Chan	nel-h	opping	g Sch	edule						١	leighb	or Ta	ble			
A = (2, 4) $B = (1, 2)$	$\{\beta,(SC,Seed)\}$										ID	sc	Seed	Overlapping Channel Set						
		Node	0	1	2	3	4	5	6			30	Seeu	Slot 0	Slot 1	Slot 2	Slot 3	Slot 4	Slot 5	Slot 6
(SC, Seed) of (SC, Seed) of	{ 4, (2, 4) }		Ch 4	Ch 2	Ch 1	Ch 0	Ch 4	Ch 3	сс	ſ	в	1	2	{4}	{2}	{3}	{0}	Ø	{0}	ø
C = (3, 2) $D = (4, 1)$		Α								\mathbf{H}	С	3	2	{4}	{4}	{1}	{2}	{4}	ø	ø
	{ 3, (4, 4) }		Ch 3	Ch 4	Ch 3	Ch 2	Ch 1	Ch 0	Prob		D	4	1	ø	{2, 4}	{3}	Ø	Ø	{3}	ø
$(\mathbf{A}) \overline{\boldsymbol{4}} \cdot \overline{\boldsymbol{-}} \cdot \overline{\boldsymbol{-}} \cdot \overline{\boldsymbol{-}} \cdot \overline{\boldsymbol{(1,2)}} \stackrel{\bullet}{=} (\mathbf{B})$	{ 2, (1, 2) }		Ch 2	Ch 1	Ch 3	Ch 0	Ch 2	Ch 4	сс	ſ	А	2	4	{4}	{2}	{3}	{0}	Ø	{0}	ø
		в								\mathbf{H}	С	3	2	{2, 4}	ø	Ø	ø	Ø	ø	ø
	{ 4, (2, 2) }		Ch 4	Ch 2	Ch 4	Ch 1	Ch 3	Ch 0	Prob		D	4	1	{2}	{2}	{3}	{1}	{2}	ø	ø
(2, 4)	{ 2, (3, 2) }		Ch 2	Ch 3	Ch 0	Ch 2	Ch 4	Ch 1	сс	ſ	А	2	4	{4}	{4}	{1}	{2}	{4}	ø	ø
		С								4	в	1	2	{2, 4}	Ø	Ø	ø	ø	ø	ø
	{ 4, (4, 2) }		Ch 4	Ch 4	Ch 1	Ch 3	Ch 0	Ch 2	Prob		D	4	1	{2}	{4}	{0}	ø	{0}	{1}	ø
	{ 1, (4, 1) }		Ch 1	Ch 4	Ch 0	Ch 1	Ch 2	Ch 3	сс	ſ	А	2	4	Ø	{2, 4}	{3}	Ø	Ø	{3}	ø
(C) → (3, 2) → (D)		D		_						\mathbf{H}	в	1	2	{2}	{2}	{3}	{1}	{2}	ø	ø
	{ 2, (2, 1) }		Ch 2	Ch 2	Ch 3	Ch 4	Ch 0	Ch 1	Prob		С	3	2	{2}	{4}	{0}	ø	{0}	{1}	ø
		1	Parity					Br	oadc	ast	-									
			Slot						Slot											

Fig. 5. Channel-hopping schedule exchange procedure performed during the Broadcast Slot over the control channel (CC) and the established neighbor table at each node. Note that only the first (SC, Seed) pair of each node needs to be broadcast.

Broadcast messages, such as ad hoc beacons, time synchronization signaling, route discovery packets (RREQ/RREP), and channel-hopping schedule exchanges can be performed during the Broadcast Slot over the common control channel (CC). In addition, channel quality probing³ based on broadcasting back-to-back short packets over data channels (DCs) can also be executed during the Broadcast Slot. If a single radio is equipped at each node, we divide the Broadcast Slot into two mini-slots, each occupying half a slot time. Broadcast messages are sent over CC in the first half of Broadcast Slot, while probing packets are transmitted over some DC in the second half. Given p data channels (DCs) available, it takes p cycles to complete the probing on all DCs. The probing will continue and keep updating current channel qualities over respective DCs. On the other hand, if multiple radios are available, we propose to dedicate the first radio interface for broadcast transmissions over CC and the remaining interface(s) for probing over DCs during the Broadcast Slot. Since two DC probings can be performed in a single Broadcast Slot, with wradios available, in average the probing over all DCs requires $\frac{p}{2(w-1)}$ cycles to be completed. For instance, in Fig. 5, with two radios and five DCs, it takes 2.5 cycles to go through all DC probings. Note that the CC is not designed for the purpose of data channel negotiations, and the usage of CC here provides a common rendezvous for control packets, which is beneficial in facilitating necessary broadcast transmissions demanded by practical network operations.

Fig. 5 depicts the channel-hopping schedule exchange scenario in a local neighborhood between four nodes, whose hopping schedules have been configured according to the proposed channel-hopping scheme (CHS). Two radios are available at each node. Since CHS configures the remaining radios based on the first interface, only the (SC, Seed) pair of the first radio is required to be announced. In other words, *the schedule exchange message set out by each node is fixed in length,* independent of available radio interfaces. This is a nice

property in terms of saving communications resource. Once receiving the first (SC, Seed) pair, a node can infer the remaining (SC, Seed) pair(s) for other interface(s) based on the same CHS algorithm. Then a complete neighbor table recording communications channels with a certain neighbor at respective time slots can be constructed, as shown in Fig. 5. Define N_i^A as the set of communicable neighbors for node A at the i^{th} slot, and O_i^{AB} as the set of overlapping channels for nodes A and B at the *i*th slot. In our example, $N_i^A = \{B, C, D\}$, while $O_0^{AB} = \{4\}, \quad O_1^{AB} = \{2\}, \quad O_2^{AB} = \{3\}, \quad O_3^{AB} = \{0\},$ $O_4^{AB} = \phi$, $O_5^{AB} = \{0\}$, and $O_6^{AB} = \phi$. Some nodes may have multiple communications channels at a certain slot. For instance, $O_1^{AD} = \{2, 4\}$ between nodes A and D at Slot 1. The neighbor tables are updated according to received schedule exchange messages and maintained locally to facilitate route discovery and data delivery.

4.2 Routing Algorithm

Given the constructed network information, we now describe the route discovery procedure. Normal route request (RREQ) and reply (RREP) handshaking process found in most ondemand ad hoc routing protocols, such as DSR and AODV [13], [21], is adopted. We modify a typical DSR RREQ packet to carry extra information helpful in determining a best route from source node S to destination node D. By extra information, we propose to have every node estimate the expected transmission time (ETT) from node S to itself. The metric ETT involves both channel qualities (rates) and overlapping conditions from hop to hop. Although packets can arrive at any time instance (over a continuous time domain), we regard those arriving after the beginning of Slot i - 1 but before Slot ias packets arriving at Slot *i*, where $0 \le i \le p + 1$. Consequently, the ETT information is represented by a vector denoted as $ETT[0], ETT[1], \dots, ETT[p+1]$ to indicate expected transmission times when packets at source node S emerge at Slot 0, Slot 1, . . ., and Slot (p + 1) respectively. Initially, node S sets ETT[i] = 0 for $i = 0, 1, \dots, p + 1$. Suppose node A receives the RREQ from node S, node A checks its neighbor table on overlapping channel sets with node S at respective time slots.

^{3.} Each node performs the channel quality probing to estimate data rates for neighboring links over all DCs.

If the overlapping channel set is non-empty at Slot *i*, then the estimated average link transmission time (LTT) is added to ETT[i]. Otherwise, the transmission needs to be postponed for, say, *k* slots, then the slot waiting time $k(t_{SDU} + t_{SDE})$ should also be counted into the calculation of ETT[i]. As the RREQ propagates further, each node should first estimate the arrival slot based on current ETT[i] carried by the received RREQ. Specifically, suppose node B receives the RREQ from node A, based on ETT[i], node B is able to estimate the arrival slot *j* by computing

$$j = \left(i + \left\lfloor \frac{ETT[i]}{t_{SDU} + t_{SDE}} \right\rfloor\right) \mod (p+2).$$
(7)

Then node B checks its neighbor table on the overlapping channel set (O_j^{AB}) with node A at Slot *j*. In case O_j^{AB} is nonempty, node B simply adds link transmission time (LTT) to ETT[i]. Otherwise, the transmission need be postponed and slot waiting time should be counted in. Algorithm 2 provides the pseudocode on RREQ forwarding mechanism and detailed ETT vector calculations exercised by CDR protocol. Based on the ETT vector carried in RREQ packet, the destination node D obtains the average ETT by calculating $\frac{\sum_{j=0}^{p+1} ETT[i]}{p+2}$, assuming equal probability for packets to arrive at any slot at node S. After gathering three RREQ packets or timeout expires, node D determines a best route with the minimum average ETT value and sends RREP back to node S along the selected route.

Algorithm 2 Forwarding Mechanism When a New RREQ Has Been Received

- 1: Suppose this new RREQ is originated from node S and received by node B from node A;
- 2: // initially *ETT*[*i*] = 0 for *i* = 0, 1, 2, . . . , *p* + 1 which are carried in the RREQ issued by node S
- 3: for $(i = 0; i \le p + 1; i + +)$ do

4: Set
$$j = (i + \lfloor \frac{ETT[i]}{(t_{SDU}+t_{SDE})} \rfloor) \mod (p+2);$$
 // arrival slot

- 5: Set $r = ETT[i] \mod (t_{SDU} + t_{SDE});$
- 6: **if** $(O_i^{AB} \neq \phi)$ then

7: Set
$$ETT[i] = ETT[i] + LTT_{avg}$$
;

8: // link transmission time (LTT) averaged from all communicable data channels in O_i^{AB}

```
9: else
```

- 10: Set k = 0;
- 11: while $(O_i^{AB} == \phi)$ do
- 12: Set $j = (j+1) \mod (p+2)$;
- 13: Set k = k + 1;
- 14: end while

15: Set
$$ETT[i] = ETT[i] - r + k(t_{SDU} + t_{SDE}) + LTT_{ava}$$

16: **end if**

- 18: Append the address of node B in RREQ;
- 19: Update the *ETT* vector $(ETT[0], ETT[1], \dots, ETT [p+1])$ in RREQ;
- 20: Rebroadcast the modified RREQ packet;

Fig. 6 displays a CDR route discovery example in a multihop network with p = 5 and w = 2. Three routes are discovered: $\{S, A, B, C, D\}$, $\{S, E, F, D\}$, and $\{S, G, H, I, D\}$. Route $\{S, E, F, D\}$ is the shortest but the overlapping slots between nodes S and E concentrate at Slot 1, 2, and 3, leading to significantly increased ETT values in $RREQ_{SE}$, which affects the resultant ETT values obtained by destination node D. Similar problem occurs in route $\{S, G, H, I, D\}$ between nodes H and I. As a result, route $\{S, A, B, C, D\}$ with the minimum average ETT value is selected. This demonstrates the route selection mechanism exercised by the proposed CDR, which successfully quantifies both the channel qualities (rates) and overlapping conditions in a unified ETT calculation model⁴.

4.3 Data Queue Management

In order to distinguish the purpose of link-layer packets, each node normally keeps a broadcast queue and a (unicast) data queue. Broadcast packets, such as ad hoc beacons, timing messages, probings, and channel schedule exchanges, are scheduled periodically and served in order. Likewise, traditionally packets in data queue are enqueued in the order of their arrival times and dequeued (served) in a first-in first-out (FIFO) manner. However, in channel-hopping wireless environments, such single data queue approach can lead to significantly extended transmission processing time. For example, in Fig. 7, three flows go through node S: $\{S, A, C\}$ (Flow 1), $\{S, B, D\}$ (Flow 2), and $\{S, B, E\}$ (Flow 3). Two packets destined for node C arrive before packets destined for nodes D and E as illustrated in the figure at time t_0 (the beginning of Slot 0). When single (data) queue approach is used, node S has to wait until Slot 5 for delivering the headof-line (HOL) packet C despite that packets D and E can be delivered earlier at Slot 2. Similar problem occurs at node B when packet E (which can be delivered at Slot 3) is blocked by packet D (which can be delivered later at Slot 4).

In light of this, we propose to implement per-destination queues to avoid blocking transmittable packets due to a HOL packet whose receiver is currently out of reach. As illustrated in Fig. 7 (lower right), node S maintains queues q_C , q_D , and q_E for destination nodes C, D, and E separately. For queue services, we perform selective round-robin (SRR) mechanism that works as follows. Recall that N_i^S is the set of communicable neighbors for node S at the i^{th} slot. Denote Q_i^S as the set of *serviceable destination queues*, we mean those queues whose next-hop nodes to their respective destinations are in set N_i^S . During each slot time, node S establishes set Q_i^S and serves the selected queues in a round-robin manner. For each queue in service, the first-in first-out (FIFO) rule is applied. Algorithm 3 provides the pseudocode for the SRR mechanism performed

^{4.} Once a packet is sent over a certain wireless channel, the transmitterreceiver pair will not perform channel switching (even when the next time slot begins) until the packet is completely processed.

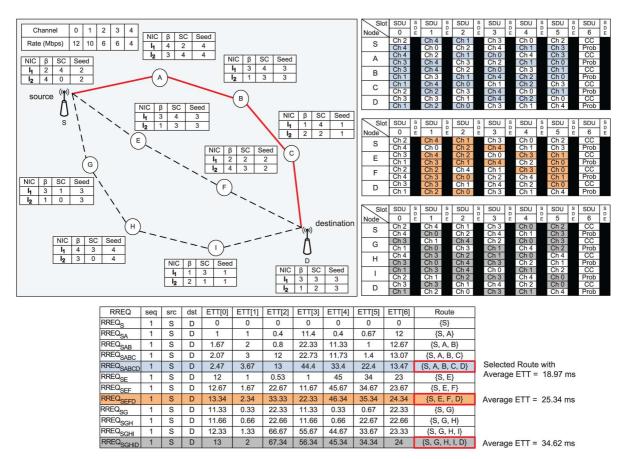


Fig. 6. Example of route selection exercised by the proposed channel-diverse routing (CDR) in a multi-hop wireless network (p = 5, $\omega = 2$). The ETT calculations (in ms) are based on the following settings: packet size S = 500 bytes, $t_{SDU} = 10$ ms and $t_{SDE} = 1$ ms.

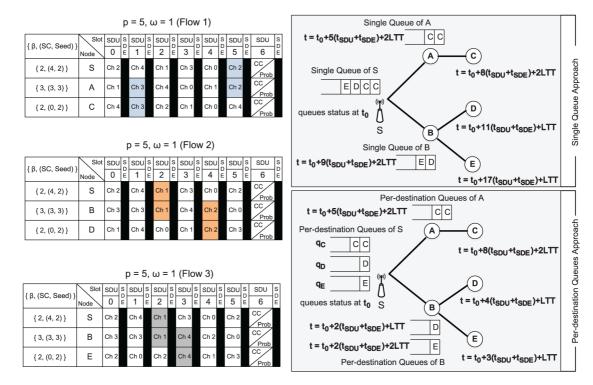


Fig. 7. Advantage of implementing per-destination queues (lower right) to accelerate data delivery by avoiding blocking transmittable packets. During each slot time, deliverable flows (selective destination queues) can be served in a round-robin manner.

Notation	Description
p	Number of available orthogonal (non-overlapping) channels
w	Number of total radio interfaces equipped at a node, where $w \le p$
t_{SDU}	Slot duration time
t_{SDE}	Channel switching delay
R_i	Link rate of channel C_i , where $0 \le i < p$
I_i^A	The <i>i</i> -th radio interface of node A, where $1 \le i \le \omega$
χ^A_i	Starting channel configured for interface I_i^A , where $0 \le \chi_i^A < p$
α_i^A	Channel seed configured for interface I_i^A , where $1 \le \alpha_i^A < p$
eta_i^A	Parity channel configured for interface I_i^A , where $0 \le \beta_i^A < p$
$K_{A^iB^j}$	Overlapping slot offset with respect to Slot 1, produced by interface I_i^A and
	interface I_j^B , where $1 \le i, j \le w$ and $0 \le K_{A^i B^j} < p$
N_i^A	Set of communicable neighbors for node A at the <i>i</i> -th slot
O_i^{AB}	Set of overlapping channels for nodes A and B at the <i>i</i> -th slot, where $0 \le i \le p+1$
Q_i^A	Set of serviceable destination queues at the <i>i</i> -th slot for node A

TABLE 1 Summary of Notations Used in This Paper

during each slot time. Our goal is to transport as many packets as possible in a single time slot. By exercising the proposed data queue management, the delivery times for packets D and E have been effectively reduced by $7(t_{SDU} + t_{SDE})$, equivalent to one cycle time, and $14(t_{SDU} + t_{SDE})$, equal to two cycle time, respectively, as shown in Fig. 7. This queue management skill is essential in realizing efficient data delivery under the channel-hopping scenario, especially for nodes that act as common relaying points and serve multiple destinations.

Algorithm 3 SRR Algorithm Performed during the *i*-th Slot Time for Node A

- 1: Construct Q_i^A with supposedly K elements; // denoted as $q_0, q_1, \ldots, q_{K-1}$
- 2: Set k = 0;
- 3: while (! end of the *i*-th slot time) do
- 4: Look up the next-hop node n_k for the destination queue q_k ; // $n_k \in N_i^A$
- 5: **if** ((\exists idle channel $C_i \in O_i^{An_k}$) && (q_k not empty)) **then**
- 6: Pop the head-of-line (HOL) packet; // dequeue operation from q_k
- 7: Complete the DCF contention procedure and transmit over channel *C_j*;
- 8: // in case multiple idle channels exist, just randomly choose one
- 9: end if
- 10: **if** (end of the *i*-th slot time) **then**
- 11: break;
- 12: end if
- 13: Set $k = (k+1) \mod K$;
- 14: end while

4.4 Summary

We have completed the design of channel-diverse routing (CDR) that operates under the channel-hopping scheme (CHS) presented in Section 3. Note that in this time-divided system, there may be ongoing transmissions on slot boundaries. In those cases, CDR lets the transmissions be completed before switching back to predefined hopping schedule. Moreover, the ETT calculations exercised by CDR are based on the given link capacities, which are not easy to obtain precisely in practice. We implement the probing mechanism to estimate link capacities in our simulative experiments, which show that certain inaccuracies do exist but are allowed. As long as the relative channel qualities are not drastically reversed, channel probing is beneficial and the probing overhead can be more than made up for by the increased overall throughput. Finally, we summarize the notations used in this paper in Table 1 for readers' convenience.

5 PERFORMANCE EVALUATION

In this section, simulative experiments are conducted to validate the proposed CDR protocol performance. We extend the ns simulator (version 2.29) to support multi-channel multiradio (MC-MR) IEEE 802.11a environments. Two-ray ground propagation model is used, and default transmit power (leading to a nominal transmission range of 250 meters) remains unchanged. Table 2 summarizes the parameter settings in our ns simulator. In IEEE 802.11a, eight (up to twelve) orthogonal channels are available, seven of which are used as data channels, denoted as Ch 0, Ch 1, ..., Ch 6 in the order of decreasing link rates (as shown in Table 2). The remaining channel, Ch7, is used as the control channel with link rate set at 6 Mbps. For comparison purpose, we implement four existing multi-channel protocols: Static (w = 2), Random Static (w = 2), FCR-MAC (w = 2) [9], and SSCH (w = 1) [3], where w indicates the number of radio(s) equipped at each node. The Static approach permanently binds the two radios to Ch 0 and Ch 1 with the highest link rates. DSR RREQs are flooded over the two channels to discover the shortest multi-hop route

Parameter	Value					
Simulator version	NS 2.29 with MC-MR extension					
Propagation model	Two-ray ground					
Transmit power	Default (0.281838 Watt)					
Transmission range	250 meters					
Number of nodes	9, 16, 25, 36, 49, 64, 81, 100					
t_{SDU} / t_{SDE}	10 ms / 1 ms					
Packet size	512 bytes					
Sending rate	600 Kbps					
Simulation time	10 seconds					
Channel capacity (Mbps)	Ch 0 (6), Ch 1 (5.4), Ch 2 (4.5), Ch 3 (4.2)					
	Ch 4 (3.6), Ch 5 (3), Ch 6 (1.8), Ch 7 (6)					

TABLE 2 Parameter Settings in Our NS Simulator

(with minimum hop count). Using the same routing mechanism as Static, Random Static (RS) organizes the total 8 channels into four sets (each containing 2 channels), and evenly divides the simulation time into four portions. All RS nodes simultaneously switch to the next channel set when a new time portion begins, so that network connectivity can be maintained and all 8 channels can be utilized. The FCR-MAC protocol dedicates one radio to Ch 7 for control traffic and performs an on-demand channel allocation (negotiation) among the other 7 data channels (Ch 0-Ch 6). The negotiation process in FCR-MAC is based on the RTS/CTS handshaking procedure. Once the data channel has been determined, the other radio switches to the selected channel for data delivery. DSR RREQs are propagated over the control channel (Ch 7) to discover the shortest multi-hop path. The SSCH mechanism, similar to our single-radio CHS, is a time-divided channelhopping scheme. In SSCH, 7 data channels (Ch 0-Ch 6) are utilized to produce two interlacing (channel, seed) pairs that determine the channel-hopping schedule followed by a node. Each (channel, seed) pair in SSCH is a pseudo-random sequence as defined in Section 3.2. Therefore the network connectivity can be guaranteed in SSCH. When there is a communications need, the intended transmitter switches one of its (channel, seed) pair to synchronize with one of the receiver's hopping schedules. Due to a lack of common channel, broadcast transmissions are difficult in SSCH. Necessary broadcast messages, such as ad hoc beacons, timing offsets, channel schedule exchanges, and DSR routing packets need be transmitted over all data channels. In the simulations, we set aside 2 ms before every time slot for these broadcast traffic. Unlike SSCH, our CDR protocol uses Ch 7 as the control channel and implements a Broadcast Slot at Slot 8 of each cycle. We simulate CDR (w = 1) and CDR (w = 2). Both SSCH and CDR employ the time-divided channel-hopping mechanism with slot duration $t_{SDU} = 10$ ms and channel switching delay $t_{SDE} = 1$ ms. In CDR, the destination collects three RREQs or waits until timeout, set at 10 ms, expires, and returns RREP with the minimum average ETT as the best route, which may not be the shortest. On the other hand, SSCH adopts the discovered shortest route with minimum hop count. Except for FCR-MAC, RTS/CTS handshaking is disabled in the simulated multi-channel approaches.

In a simulated network with N wireless nodes (N ranges from 9 to 100) randomly deployed, we arbitrarily generate $\left|\frac{N}{4}\right|$ CBR traffic flows (proportional to the number of nodes) with per packet size set at 512 bytes and sending rate at 600 Kbps. For a certain number of generated flows, we create 10 random source-destination configurations and obtain an averaged value produced by respective approaches. Fig. 8 plots the performance results in terms of first-bit latency, system throughput, and average end-to-end delay under various network sizes (up to 100 wireless nodes). From this figure, our CDR approach has the first-bit latency lower than SSCH but higher than FCR-MAC and Static methods. Both SSCH and CDR are channel-hopping based schemes, which inherently incur higher first-bit latency because of the longer time required to discover routes for flows. In the case of SSCH, the latency becomes more pronounced due to the lack of a common broadcasting channel for exchanging channelhopping schedules and other control information between wireless nodes. In contrast, our CDR has a dedicated control channel and broadcast slot for exchanging control information, including channel-hopping schedules, in a timely manner (within one cyclic timeframe). As such, both CDR (w = 2) and CDR (w = 1) not only require less time than SSCH for the first packet to arrive, but also produce better system throughput than SSCH. On the other hand, although FCR-MAC and Static methods have the least first-bit latency, they do not perform well in terms of system throughput and average delay due to the inappropriate non-quality-aware routes chosen for their flows. In our CDR approach, once the routes are readily available, subsequent flow traffic transmissions become smooth and efficient, leading to the lowest average end-to-end delay despite requiring higher first-bit latency than FCR-MAC and Static methods. Consequently, the results in Fig. 8 demonstrate that both CDR (w = 2) and CDR (w = 1) outperform the other schemes with the highest system throughput produced, ensuring the CDR protocol designs.

To analyze the protocol behaviors in more detail, we observe a specific network configuration, as depicted in Fig. 9. In the figure, 25 nodes numbered as $0, 1, \ldots, 24$ and 8 sourcedestination pairs denoted as $S_1/D_1, S_2/D_2, \ldots, S_8/D_8$ are displayed. The eight flows are generated in order. We observe the aggregate throughput under respective multi-channel

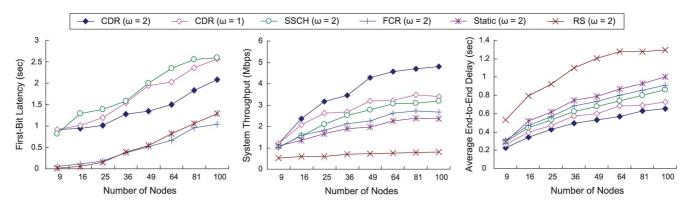


Fig. 8. First-bit latency, system throughput, and average end-to-end delay comparison under static IEEE 802.11a multi-hop wireless networks with number of nodes ranging from 9 to 100.

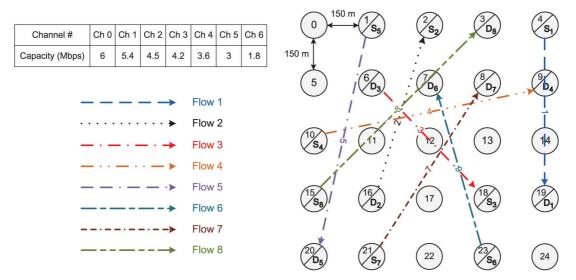


Fig. 9. Network configuration with eight randomly-generated traffic flows.

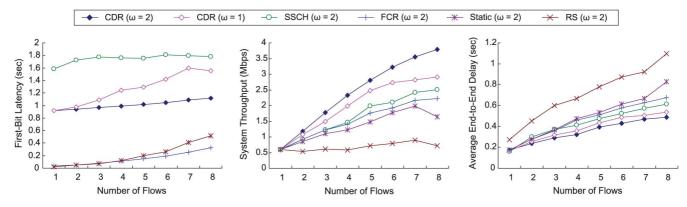


Fig. 10. First-bit latency, system throughput, and average end-to-end delay comparison against number of generated flows in a static 25-node multi-hop wireless network.

approaches as traffic flows saturate the system gradually. Fig. 10 shows the result. In terms of system throughput and average end-to-end delay, our CDR approach outperforms others at all times, while Random Static (RS) produces the lowest system throughput and highest average delay. FCR-MAC and SSCH outperform Static marginally, and the benefit brought by FCR-MAC and SSCH is insufficient to compensate for the overhead they incur. When we observe the first-bit latency in Fig. 10, SSCH requires the longest first-bit arrival time due to its time-consuming discovery of channel-hopping schedules between wireless nodes. Despite not having the shortest first-bit arrival time, our CDR approach yields the best system throughput.

In order to further investigate the resultant performance achieved by respective approaches, we analyze the route distribution and channel usage in Fig. 11. We plot the selected routes for all eight flows and list the channels used by all wireless links. In CDR and SSCH, we also mark the transmission

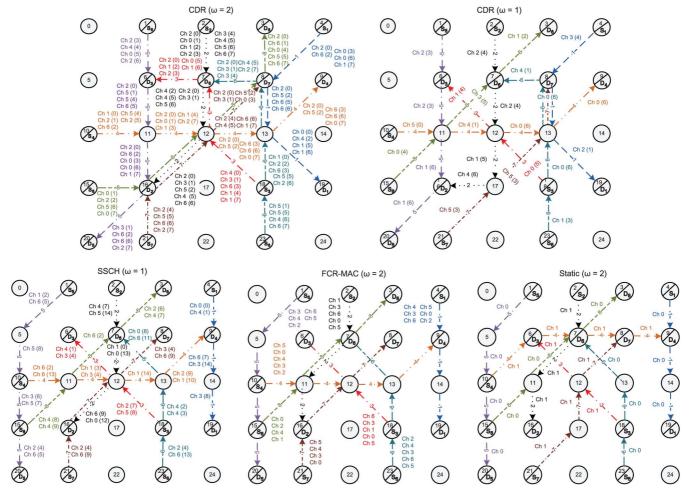


Fig. 11. Route distribution and channel usage under respective multi-channel approaches when eight flows saturate the system.

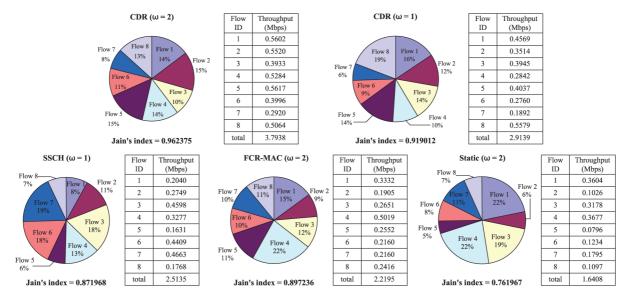


Fig. 12. Analysis of per-flow throughput under respective multi-channel approaches.

slots in parentheses. For example, Ch 2 (0) indicates the transmission can be performed at Slot 0 over Ch 2. As we can see from the figure, CDR does not always select the shortest routes, such as flows 3, 6, and 8. Even with the same number of hops, CDR tends to travel through channel-diverse routes to mitigate both intra-flow and inter-flow interferences. This

channel-diversity is beneficial as well in balancing throughput share among flows, as illustrated in Fig. 12. We use Jain's fairness index as defined by $\frac{\left(\sum_{i=1}^{n} X_{i}\right)^{2}}{n \times \sum_{i=1}^{n} X_{i}^{2}}$, where *n* is the number of flows and X_{i} is the throughput of flow *i*. Among all simulated approaches, CDR is able to maintain the highest

fairness while producing the most system throughput. Three main features of CDR attribute to this result. First, the design of CHS exploits channel randomness with guaranteed network connectivity (no logical partition) and makes sure overlapping slots are evenly distributed. Second, the corresponding CDR protocol is channel quality aware, taking care of link rates and overlapping conditions along the route. Third, our data queue management mechanism avoids blocking deliverable packets, such that a relaying node can serve as many destinations as possible in a single time slot. When traffic is heavy and multiple flows go through the same relaying node, our SRR scheme nicely handles traffic demands without wasting slot time.

6 CONCLUSION

In this paper, we proposed a connectivity-preserving multiradio channel-hopping scheme (CHS) and its corresponding channel-diverse routing (CDR) protocol for static multi-hop wireless environments with multiple orthogonal channels available. Most prior multi-channel protocols focus on the channel allocation mechanism, while leaving the multi-hop routing issue unaddressed. We observed the two issues should be jointly considered, and thus designed our multichannel protocol suite to include both the channel allocation (CHS) and multi-hop routing (CDR) mechanisms. Simulation results have shown that the proposed approach effectively improved system throughput and achieved better balanced throughput share among flows than other simulated multichannel protocols in a noticeable way.

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